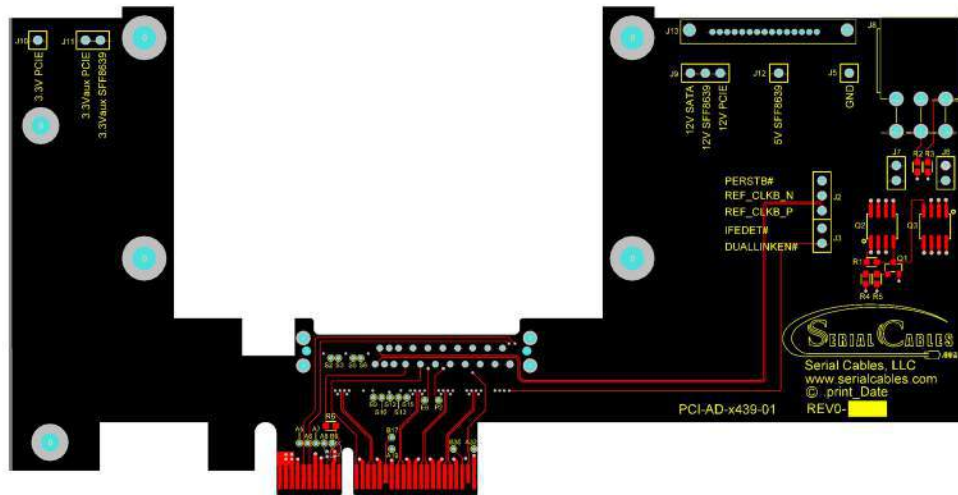




For Sales Information:
sales@serialcables.com
(303)495-2320
<http://www.serialcables.com>



J1 is the sff8639 receptacle

J2 and J3 are access pins for signals

- PERSTB#
- REFCLKB_P
- REFCLKB_N
- IFDET#
- DUALLINKEN#

J5 is a ground pin.

J6 is a jumper to get sff8639 5Volts from the Quarch Power

J7 is a jumper to get sff8639 12Volts from the Quarch Power

J8 is the Quarch power plug

J9 is a jumper that allows the user to get sff8639 12V from either the PCIE card edge or the 12V from the

J10 is an access point to PCIE 3.3Volts

J11 is a jumper to allow the user to connect sff8639 3.3 Vaux to the PCIE card edge 3.3 Vaux

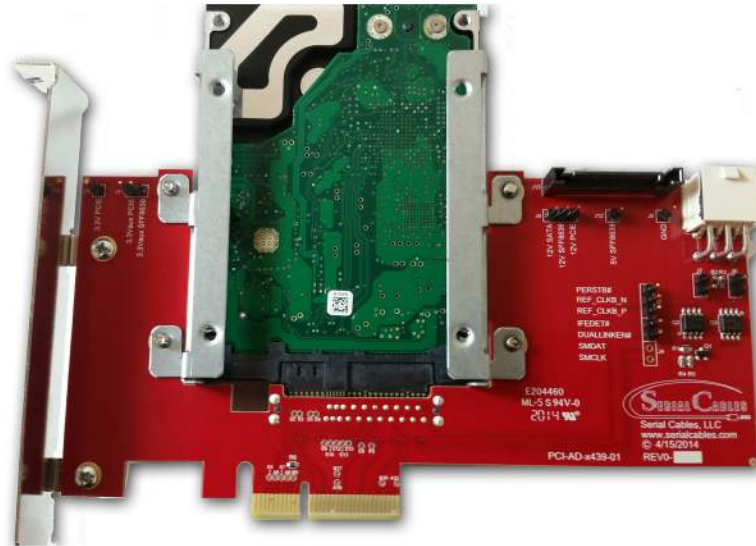
J13 is the SATA power header

Test Points:

A5 , A6, A7, A8, A19, A32, B9, B17, B30 correlate to the pin out on the PCIE card edge connector. There are side A and side B signals. These pins give access to the following PCIE signals:

- A5 = JTAG2
- A6 = JTAG3
- A7 = JTAG4
- A8 = JTAG5
- A19 = RSVD
- A32 = RSVD
- B9 = JTAG1
- B17 = PRSNT2#
- B30 = RSVD

S2, S3, S5, S6, S9, S10, S12, S13, S15, E6, P2 correlate to the pin out of the sff8639 connector. These pins are currently not defined with the exception of E6 which is reserved.



Powering the SFF8639 DUT can come from multiple sources including the Quarch power margining tool. Jumper J9 provides power to the DUT 12V from either the mother board or from the SATA power plug J13. The DUT 5V can come from jumper J12 or from the Quarch power margining tool and a jumper on J6. Be sure that DUT is power is connected to only one source at a time. For example if DUT 5V is being provided by the Quarch power margining tool make sure no power is applied to J12. The same applies to DUT 12V and jumper J9.

The SFF8639 3.3Vaux can come from either a bench top power supply or the system mother board. To use a bench top power supply connect 3.3V to the pin labeled "3.3Vaux SFF8639" on jumper J11. If DUT 3.3Vaux from the mother board is desired put a jumper on J11. J10 provides access to the mother board 3.3v supply.

Power margining can be achieved by using the Quarch Power Module. Connect the Quarch Power Module to the adapter using Jumper J8. Place a jumper on J7 to power the SFF8639 DUT with 12V from the Quarch Power Module. Make sure that there are no jumpers on J9. A jumper on J6 will provide the SFF8639 DUT with 5V from the Quarch Power Module. Make sure power is not being provided from J5 while using the 5V from the power module. Both 12V and 5V are synchronized to the PCIe 12Volt supply. Meaning the Quarch Power module will not provide power until it detects the 12V PCIe power.