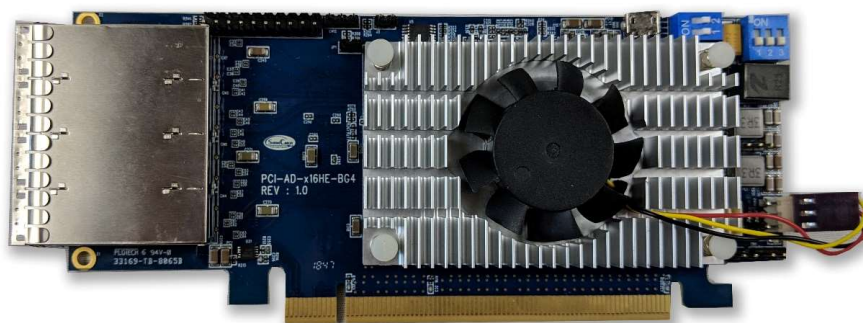


PCI-AD-x16HE-BG4 User's Guide



J3

ON: uP in USB boot mode(disable uP FW)
OFF: uP is normal operation(default)

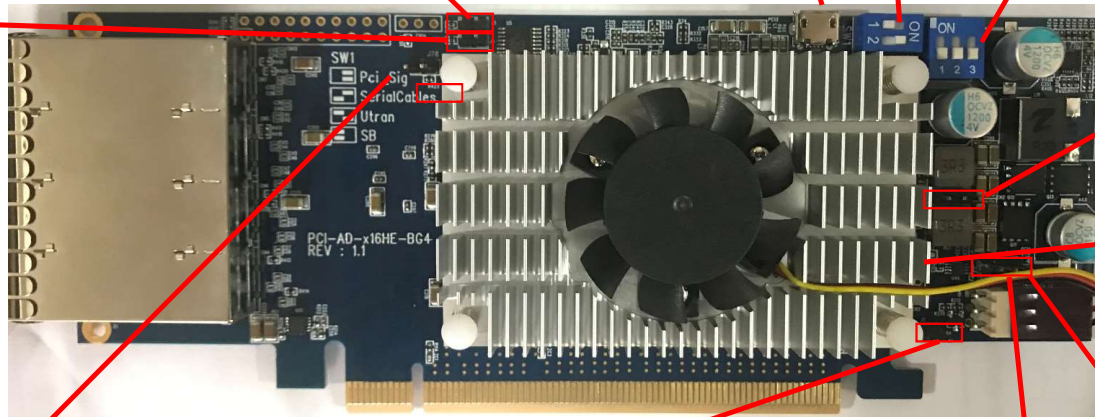
J4

ON: uP without SDB of switch control
OFF: uP is allowed to access switch information via SDB(default)

Side-band select switch

Micro USB connector

Reserve for further used



CN2

Header for Atlas UART
(Required FW)

TX	RX	GND
----	----	-----

Blue LED for switch heartbeat.
Without FW, default state is ON

RED LED for switch error

J78

ON: FW upgrading for uP
OFF: uP in normal operation(default)

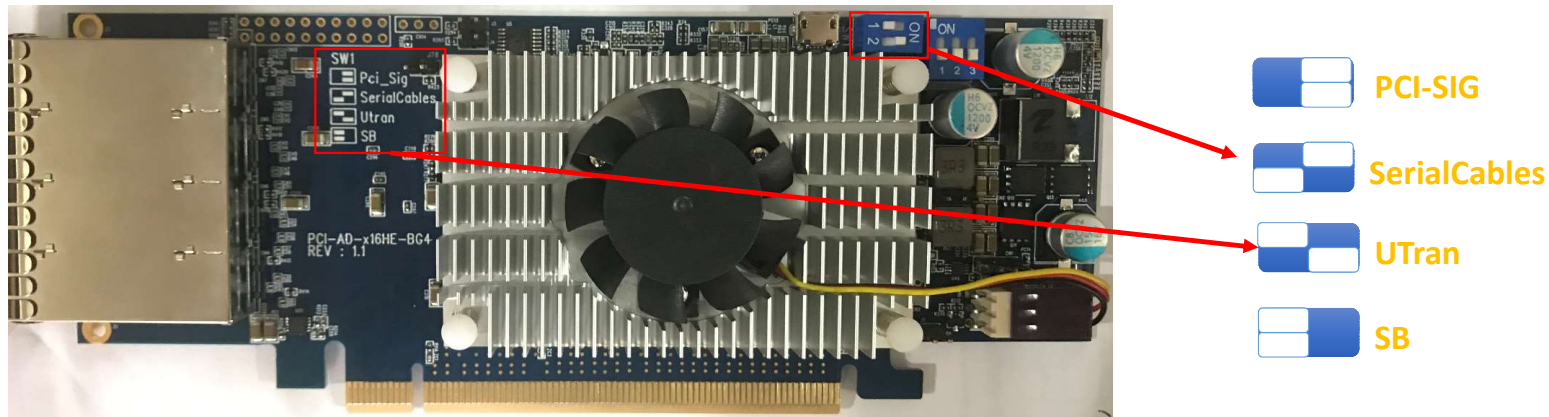
J2

ON: Disable Atlas loading SBR(configuration)
OFF: Atlas loading SBR enable(default)

CN1

Header for Atlas SDB UART

TX	RX	GND
----	----	-----



Host card side-band mode				
	PCI-SIG	SC	UT	SB
A1	CADDR	CLK_0_N	CLK_0_N	CLK_0_N
A2	CABLE_INT#	CLK_0_P	CLK_0_P	CLK_0_P
B1	VCT(NC)	CLK_4_N	DP_EN#_0	DP_EN#_0
B2	CABLE_PRE#	CLK_4_P	*drive present#	*drive present#
C1	uP_SCL	ATLAS_SCL	ATLAS_SCL	ATLAS_SCL
C2	uP_SDA	ATLAS_SDA	ATLAS_SDA	ATLAS_SDA
D1	VACT	PERST#_0	PERST#_0	PERST#_0
D2	VMAN	PERST#_1	PERST#_1	PERST#_1

A host card reset is required to apply the selected side-band setting.

1. In SB mode, the C1/C2 are always kept at “high state” as requested to power on the SB switch board.
2. Other side-bands are “NC” in the SB switch board design.

Atlas EXT Host card commands list

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>help
Cmd Help Menu
fdl :
  Xmodem download image.
  - Usage: fdl sw

lsd :
  Show environmental conditions information.
  - Usage: lsd

pwmctrl :
  Switch fan pwm ctrl.
  - Usage: pwmctrl duty(D)
  - duty(D) : duty shoule be 0 ~ 100

dr :
  Dump switch-specific registers.
  - Usage: dr register(H) [count(H)]
  - register(H) : register shoule be 0x00000000 ~ 0xFFFFFFFF
  - count(H) : count shoule be 0x00000000 ~ 0xFFFFFFFF

dp :
  Dump switch port-specific registers.
  - Usage: dp port_number(D)
  - port_number(D) : port_number shoule be 0 ~ 31

df :
  Dump switch-specific flash.
  - Usage: df address(H) [count(H)]
  - address(D) : address shoule be 0x00000000 ~ 0xFFFFFFFF
  - count(H) : count shoule be 0x00000000 ~ 0xFFFFFFFF

setmode :
  Set mode of switch controller board.
  - Usage: setmode <mode(D)>
  - mode(D) : mode number shoule be 1 ~ 8

showport :
  Display link speed and link width information.
  - Usage: showport

showmode :
  Show mode information of switch controller board in system.
  - Usage: showmode

scan :
  Scan device of i2c bus.
  - Usage: scan

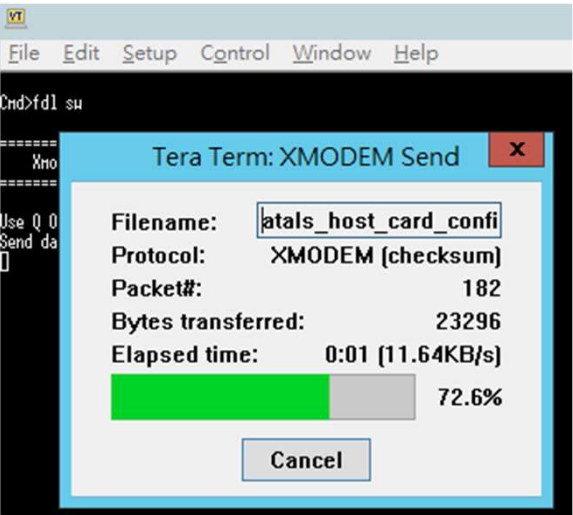
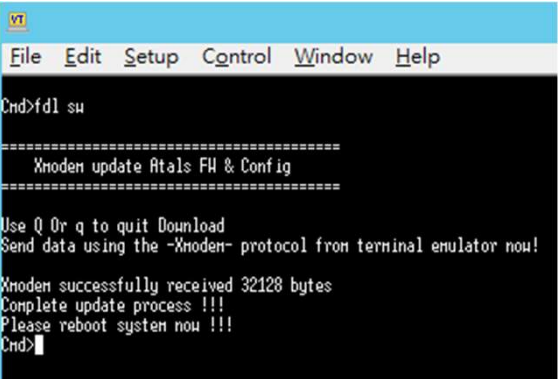
ver :
  Show microcontroller firmware version.
  - Usage: ver

reset :
  System reset.
  - Usage: reset
```

fdl sw

Xmodem download image.(Firmware update and config for Atlas)

-Usage: fdl sw



lsd

Show environmental conditions information.

- Usage: lsd

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>lsd
Thermal:
  Switch Temperture 1 : 41 degree
Fan Speed:
  Switch Fan : 6263 rpm
Voltage Sensor:
  12V Voltage : 12048 mV
  1.8V Voltage : 1790 mV
  0.9V Voltage : 863 mV
```

pwmctrl

Control the PWM duty for FAN-sink on the Atlas switch

- Usage: pwmctrl duty(D)
- duty(D) : duty shoule be 0 ~ 100

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>pwmctrl 100
```

It isn't suggested to adjust FAN speed manually, it may cause the Atlas switch over-heat

dr

Dump switch-specific registers.

- Usage: dr register(H) [count(H)]
- register(H) : register shoule be 0x00000000 ~ 0xFFFFFFFF
- count(H) : count shoule be 0x00000000 ~ 0xFFFFFFFF

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>dr 60800000
60800000:00010000 00010000 00100000 060400a0
60800010:00000000 00000000 00000000 000001f1
60800020:0000ffff 0001ffff 00000000 00000000
60800030:00000000 00000040 00000000 00000100
60800040:c8034801 00000008 01866805 00000000
60800050:00000000 00000000 00000000 00000000
60800060:00000000 00000000 0052a410 012c8004
60800070:00000810 00436d04 01030000 00000060
60800080:00001508 00000000 00000000 00050840
60800090:00000000 01800f1e 001e0004 00000000
608000a0:00000000 0000000d a0481000 00000000
608000b0:00000000 00000000 00000000 00000000
608000c0:00000000 00000000 00000000 00000000
608000d0:00000000 00000000 00000000 00000000
608000e0:00000000 00000000 00000000 00000000
608000f0:00000000 00000000 00000000 00000000
```

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>dr 60800000 4
60800000:c0121000
```

dp

Dump switch port-specific registers.

- Usage: dp port_number(D)
- port_number(D) : port_number should be 0 ~ 31

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>dp 0
60800000:c0121000 00100000 060400a0 00010000
60800010:00000000 00000000 00000000 000001f1
60800020:0000ffff 0001fff1 00000000 00000000
60800030:00000000 00000040 00000000 00000100
60800040:c8034801 00000008 01866805 00000000
60800050:00000000 00000000 00000000 00000000
60800060:00000000 00000000 0052a410 012c8004
60800070:00000810 00436d04 01030000 00000060
60800080:00001508 00000000 00000000 00050840
60800090:00000000 01800f1e 001e0004 00000000
608000a0:00000000 0000000d a0481000 00000000
608000b0:00000000 00000000 00000000 00000000
608000c0:00000000 00000000 00000000 00000000
608000d0:00010000 00010000 00000000 00000000
```


df

Dump switch-specific flash.

- Usage: df address(H) [count(H)]
- address(D) : address should be 0x00000000 ~ 0xFFFFFFFF
- count(H) : count should be 0x00000000 ~ 0xFFFFFFFF

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>df 400
00000400:c43d10c0 fc010000 c8010000 c4030000
00000410:10000000 00000000 01000000 00000000
00000420:01000000 00000000 01000000 00000000
00000430:01000000 00000000 01000000 00000000
00000440:01000000 00000000 01000000 d4030000
00000450:80090000 00000000 00000000 00830020
00000460:49000000 00000000 00004c01 80a00015
00000470:00000000 00000000 a1c0a3c0 00000117
00000480:09091585 11040000 00000c00 00000000
00000490:00000000 00000000 00000000 00000000
000004a0:00000000 00000000 00000000 00000000
000004b0:00000000 0000c029 00009fe5 00f0a0e1
000004c0:03000000 00000000 00000000 00000000
000004d0:00000000 00000000 00000000 00000000
000004e0:02000000 00000000 00000000 00000000
000004f0:00000000 00000000 00000000 00000000
Cmd>df 400 4
00000400:c43d10c0
```

Setmode

Set mode of switch controller board.

- Usage: setmode <mode(D)>
- mode(D) : mode number should be 1 ~ 8

```
Cmd>
Cmd>setmode 8
Image size = d58
Erase block region #0 at offset 400
Write block erase command into control register : Done
Issue the block erase command to the flash :Done
```

It will start to write the selected mode(configuration) into Atlas for a while, and then it required server cold reset(power cycle the server rather than reset the server) to apply the setting

```
Verify Offset:00000d10, Buffer value:1700fa00, Flash value:1700fa00
Verify Offset:00000d14, Buffer value:a003047b, Flash value:a003047b
Verify Offset:00000d18, Buffer value:1700fa00, Flash value:1700fa00
Verify Offset:00000d1c, Buffer value:a803047b, Flash value:a803047b
Verify Offset:00000d20, Buffer value:1700fa00, Flash value:1700fa00
Verify Offset:00000d24, Buffer value:3800047b, Flash value:3800047b
Verify Offset:00000d28, Buffer value:02000000, Flash value:02000000
Verify Offset:00000d2c, Buffer value:3c00047b, Flash value:3c00047b
Verify Offset:00000d30, Buffer value:8002a080, Flash value:8002a080
Verify Offset:00000d34, Buffer value:3800047b, Flash value:3800047b
Verify Offset:00000d38, Buffer value:07000000, Flash value:07000000
Verify Offset:00000d3c, Buffer value:3c00047b, Flash value:3c00047b
Verify Offset:00000d40, Buffer value:8542a180, Flash value:8542a180
Verify Offset:00000d44, Buffer value:3c00047b, Flash value:3c00047b
Verify Offset:00000d48, Buffer value:8542a100, Flash value:8542a100
Verify Offset:00000d4c, Buffer value:c803047b, Flash value:c803047b
Verify Offset:00000d50, Buffer value:400b0000, Flash value:400b0000
Verify Offset:00000d54, Buffer value:19000000, Flash value:19000000
Verify success.
Set operation mode to mode 8.
Need to reset controller to take effect.
```

Mode 1 Hx16I

Mode 2 Hx16N

Mode 3 Hx8I

Mode 4 Hx8N

Mode 5 Hx4I

Mode 6 Hx4N

Mode 7 Hx2I

Mode 8 Hx2N

“I” means support SRIS

“N” means support SRNS

Showport

Display link speed and link width information.

-Usage: showport

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>showport
Port 0:link speed = Gen3, link width = 16
Port 16:link speed = Gen1, link width = 0
Port 18:link speed = Gen1, link width = 0
Port 20:link speed = Gen1, link width = 0
Port 22:link speed = Gen1, link width = 0
Port 24:link speed = Gen1, link width = 0
Port 26:link speed = Gen1, link width = 0
Port 28:link speed = Gen1, link width = 0
Port 30:link speed = Gen1, link width = 0
Port 32:link speed = Gen1, link width = 0
```

Showmode

Show mode information of switch controller board in system.

-Usage: showmode

```
VT COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>showmode
PCIe switch mode 8
```

Scan

Scan device of i2c bus.

- Usage: scan

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>scan

Scan I2C channel 0 devices ....
Device address:0x40 found
Device address:0x42 found
Device address:0xa2 found
```

ver

Show microcontroller firmware version.

- Usage: ver

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>ver

Company : SerialCables
Model : Atals host card
Version : 0.0.6 Date : May 13 2019 11:19:58
```

reset

System reset.

- Usage: reset

```
COM6 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Cmd>reset
System Reset...
```

Install USB Driver for CLI

Step1: Download and install the CDC driver for unidentified device (VID_03EB&PID_2018)

Available at: https://www.serialcables.com/wp-content/uploads/2018/11/SynergyUSBCDC_20180518.rar

Note: No USB driver is required for Windows 10 and Linux

