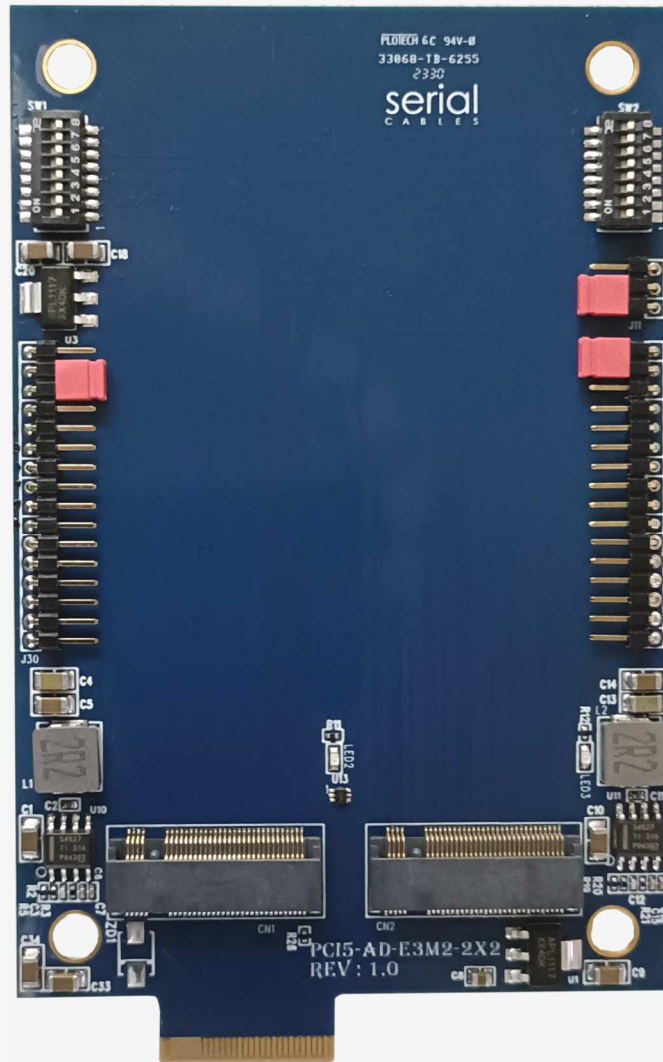




PCI5-AD-E3M2-2x2



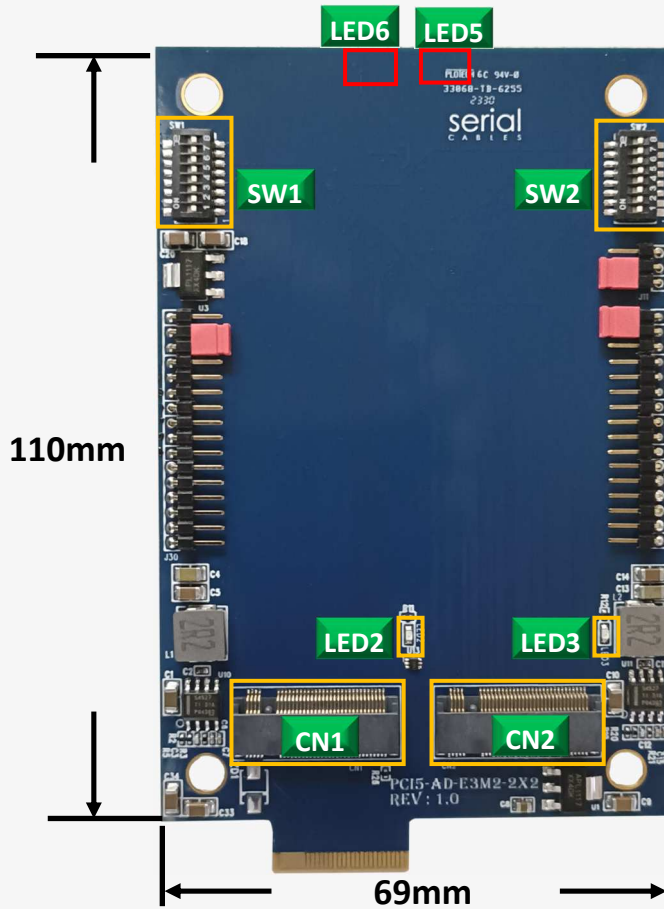
User's Manual

REV: 1.0

Aug. 2023



Function Description For Connectors And LEDs



Location	Descriptions
CN1/CN2	PCIe Gen5 M.2 Connectors. Height=3.2mm CN1—>EDSFF_Lane:0:1 CN2—>EDSFF_Lane:2:3
LED2/LED3	Green M.2 Activity LEDs
LED5	Red Host LED. Controlled by EDSFF connector Pins_A11
LED6	Blue 3.3V Power LED.
SW1/SW2	Piano switch, refer to page 4 for more detail descriptions



Function Description For Headers

J11

Name
P1V8
M2_VIO
P3V3

J9

Pins	Name
M.2_Pin40	M2_SMCLK_0
	M2_SMCLK
M.2_Pin40	M2_SMCLK_1

J4

Pins	Name
M.2_Pin40	M2_SMCLK_1
M.2_Pin42	M2_SMDAT_1
M.2_Pin44	M2_ALERT#_1
	GND

J5

Pins	Name
	GND
M.2_Pin56	M2_MFG_DATA_1
M.2_Pin58	M2_MFG_CLOCK_1

J15

Pins	Name
	GND
M.2_Pin8	M2_PLN#_1
M.2_Pin32	M2_PLA_S3#_1
M.2_Pin68	M2_SUSCLK_1
M.2_Pin73	M2_VIOCFG_1
M.2_Pin54	M2_PEWAKE#_1

J10

Pins	Name
M.2_Pin42	M2_SMDAT_1
	M2_SMDAT
M.2_Pin42	M2_SMDAT_0

J6

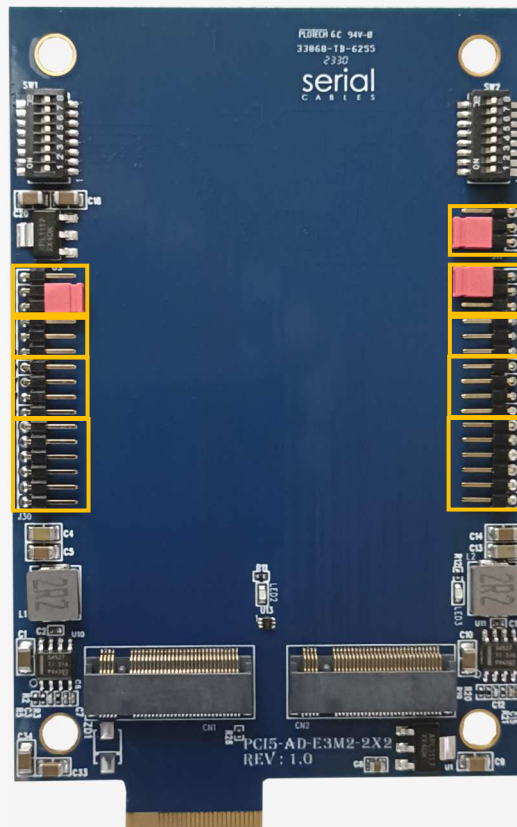
Pins	Name
M.2_Pin40	M2_SMCLK_0
M.2_Pin42	M2_SMDAT_0
M.2_Pin44	M2_ALERT#_0
	GND

J7

Pins	Name
	GND
M.2_Pin56	M2_MFG_DATA_0
M.2_Pin58	M2_MFG_CLOCK_0

J30

Pins	Name
	GND
M.2_Pin8	M2_PLN#_0
M.2_Pin32	M2_PLA_S3#_0
M.2_Pin68	M2_SUSCLK_0
M.2_Pin73	M2_VIOCFG_0
M.2_Pin54	M2_PEWAKE#_0





Function Description For Piano Switch

SW1		
Pins	Pins	Description
16	15	ON: Disable M.2 3.3V_1 output
14	13	ON: Disable M.2 3.3V_0 output
12	11	ON: Set M.2_PWRDIS_1 to be "H"
10	9	ON: Set M.2_PWRDIS_0 to be "H"
8	7	ON: Set EDSFF_PERST#_1 to be "H"
6	5	ON: Set EDSFF_PERST#_0 to be "H"
4	3	ON: Set M.2_1 PERST# to be "L"
2	1	ON: Set M.2_0 PERST# to be "L"

SW2		
Pins	Pins	Description
16	15	ON: Set TMP112A_ADD0 to "L"
14	13	ON: Set EDSFF_PWRDIS to "H"
12	11	ON: Set EDSFF_SMDAT with 3.3V PU
10	9	ON: Set EDSFF_SMCLK with 3.3V PU
8	7	ON: Set M.2_PRSENT#_1 to "L"
6	5	ON: Set M.2_PRSENT#_0 to "L"
4	3	ON: Set M.2_VIO1V8_1 to P1V8
2	1	ON: Set M.2_VIO1V8_0 to P1V8