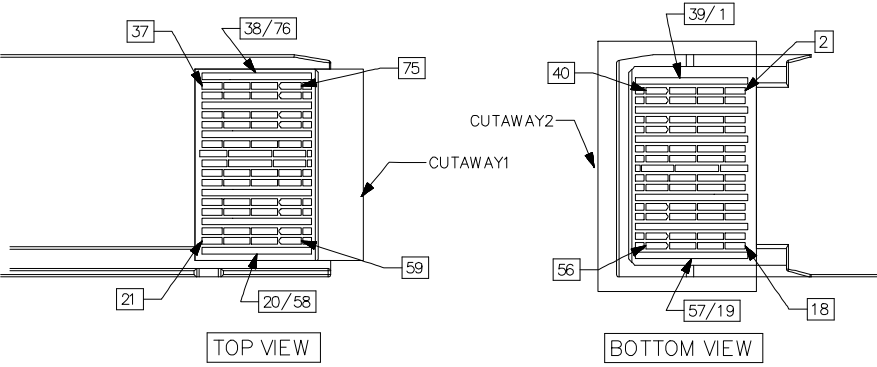


WIRING TABLE

P0 QSFP-DD		P1 MCIO 4X	
GND	1	A7	GND
TX2N	2	A6	PERn1
TX2P	3	A5	PERp1
GND	4	A4/A19	GND
TX4N	5	A18	PERn3
TX4P	6	A17	PERp3
GND	7	A16/B7	GND
HLED0	8		
PERST#0	9	A11	BPB_RST#_0
PERST#2	10	A12	BPB_RST#_1
SCL0	11	B11	HOST_SCL_0
SDA0	12	B12	HOST_SDA_0
GND	13	B13	GND
RX3P	14	B14	PETp2
RX3N	15	B15	PEtn2
GND	16	B16/B1	GND
RX1P	17	B2	PETp0
RX1N	18	B3	PEtn0
GND	19	B4	GND
GND	20	B7	GND
RX2N	21	B6	PEtn1
RX2P	22	B5	PETp1
GND	23	B4/B7	GND
RX4N	24	B18	PEtn3
RX4P	25	B17	PETp3
GND	26	B16/A10	GND
CLKN3	27	A9	BPB_CLK_1_N
CLKP3	28	A8	BPB_CLK_1_P
GND	29	B7/A7	GND
CLKN2	30	B8	BPB_CLK_0_P
CLKP2	31	B9	BPB_CLK_0_N
GND	32	B10/A13	GND
TX3P	33	A14	PERp2
TX3N	34	A15	PERn2
GND	35	A16/A1	GND
TX1P	36	A2	PERp0
TX1N	37	A3	PERn0
GND	38	A4	GND

P0 QSFP-DD		P2 MCIO 4X	
GND	39	A7	GND
TX6N	40	A6	PERn1
TX6P	41	A5	PERp1
GND	42	A4/A19	GND
TX8N	43	A18	PERn3
TX8P	44	A17	PERp3
GND	45	A16/B7	GND
CLKP0	46	B8	BPB_CLK_0_P
CLKN0	47	B9	BPB_CLK_0_N
GND	48	B10/A7	GND
CKLP1	49	A8	BPB_CLK_1_P
CKLP1	50	A9	BPB_CLK_1_N
GND	51	B13/A10	GND
RX7P	52	B14	PETp2
RX7N	53	B15	PEtn2
GND	54	B16/B1	GND
RX5P	55	B2	PETp0
RX5N	56	B3	PEtn0
GND	57	B4	GND
GND	58	B7	GND
RX6N	59	B6	PEtn1
RX6P	60	B5	PETp1
GND	61	B4/B7	GND
RX8N	62	B18	PEtn3
RX8P	63	B17	PETp3
GND	64	B16	GND
SCL1	65	B12	HOST_SDA_0
SDA1	66	B11	HOST_SCL_0
PERST#3	67	A12	BPB_RST#_1
PERST#1	68	A11	BPB_RST#_0
HLED1	69		
GND	70	A13	GND
TX7P	71	A14	PERp2
TX7N	72	A15	PERn2
GND	73	A16/A1	GND
TX5P	74	A2	PERp0
TX5N	75	A3	PERn0
GND	76	A4	GND



TITLE		*2 MCIO x4 85ohm to *1 x8 QSFP-DD				SHEET	2/2
CUSTOMER		CUSTOMER P/N	QDD-MCIOX4X2-0. 5M	DRAWING	chenlingmin		
DWG NO.	WIPT20210730001	UNIT	SCALE	REV	VIEW	CHECKED	liupeng
IPT P/N		mm	NONE	F		APPROVAL	

TEST CONDITIONS:
1.100% OPEN & SHORT & MISS-WIRING & INTERMITTENCE TEST.
2.CONDUCTIVE RESISTANCE: 3Ω MAX.
3.INSULATION RESISTANCE: 10 MΩ
4.HI-POT TEST: 300VDC FOR 10ms.
5.THIS PRODUCT MEETS PCIE 5.0.