

# PCI4-AD-x16HE-MG4 MS X16 EXT Host Adapter Card



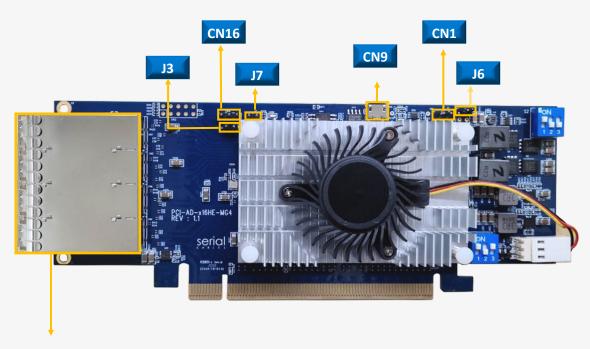
User's Manual

**REV: 1.0** 

Jan. 2021



### **Headers And Connectors**



#### **Quad Ports MiniSAS HD**

SFF8674 connector

| Headers | Description  | Pinout     |  |  |
|---------|--|------------|--|--|
| J6      | ON: Force Switchtec entering boot recovery 1                             |            |  |  |
| 30      | OFF: Switchtec loading default FW image as normal operation (default)    |            |  |  |
| CN1     | Switchtec UART I/F   | TX/RX/GND  |  |  |
| CIVI    | UART with 3.3V TTL signals level   | INJINAJOND |  |  |
| CN9     | MicroUSB port for executing uP CLI commands                              |            |  |  |
| J7      | ON: uP in FW upgrading mode  |            |  |  |
| CN16    | Reserved I/F for uP FW debugging   | TX/RX/GND  |  |  |
| J3      | ON: ISP mode for uP FW programming OFF: uP in normal operation (default) |            |  |  |



### **Side-band Modes Selection**



#### **Switch Slide S2**

| POS | Description  |  |  |  |  |  |
|-----|--|--|--|--|--|--|
| 2 3 | Description  |  |  |  |  |  |
|     | Target mode and select Side-band mode to                             |  |  |  |  |  |
|     | PCI-SIG in SFF8674 connectors (*)                                    |  |  |  |  |  |
|     | Host mode and select Side-band mode to PCI-SIG in SFF8674 connectors |  |  |  |  |  |
|     | Host mode and select Side-band mode to SC in SFF8674 connectors      |  |  |  |  |  |

| Side-band mode |            |          |  |  |  |
|----------------|------------|----------|--|--|--|
|                | PCI-SIG    | sc       |  |  |  |
| A1             | CADDR      | CLK_0_N  |  |  |  |
| A2             | CABLE_INT# | CLK_0_P  |  |  |  |
| B1             | VCT(NC)    | CLK_1_N  |  |  |  |
| B2             | CABLE_PRE# | CLK_1_P  |  |  |  |
| C1             | CMI_SCL    | CMI_SCL  |  |  |  |
| C2             | CMI_SDA    | CMI_SDA  |  |  |  |
| D1             | VACT       | PERST#_0 |  |  |  |
| D2             | VMAN       | PERST#_1 |  |  |  |

<sup>\*</sup>Note: Target mode support in Port bifurcation mode 9 with PCI-SIG side band mode



### **Bifurcation Modes Selection**

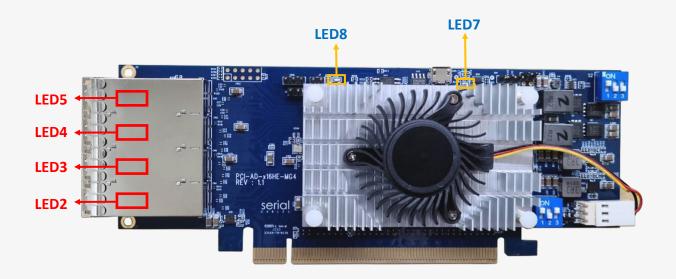


#### Switch Slide S1 and S2

| S2 S1<br>POS POS<br>1 1 2 3 | Mode | Host/<br>Target | Description                              |  |  |  |  |
|-----------------------------|------|-----------------|--|--|--|--|--|
|                             | 1    |                 | SRNS: Set SFF8674 to one x16 link        |  |  |  |  |
|                             | 2    |                 | <u>SRNS</u> : Set SFF8674 to Two x8 link |  |  |  |  |
| 3                           |      |                 | SRNS: Set SFF8674 to Four x4 link        |  |  |  |  |
|                             | 4    | Host            | SRNS: Set SFF8674 to Eight x2 link       |  |  |  |  |
|                             | 5    | 11030           | SRIS: Set SFF8674 to one x16 link        |  |  |  |  |
|                             | 6    |                 | SRIS: Set SFF8674 to Two x8 link         |  |  |  |  |
|                             | 7    |                 | SRIS: Set SFF8674 to Four x4 link        |  |  |  |  |
|                             | 8    |                 | SRIS: Set SFF8674 to Eight x2 link       |  |  |  |  |
|                             | 9    | Target          | SRIS: Set SFF8674 to one x16 link        |  |  |  |  |



# **Function Description For LEDs**



| Location    | Color | Description  |
|-------------|-------|--|
| LED7        | Blue  | Switchtec Heartbeat LED Blinking: Indicates Switchtec loading firmware successfully and working correctly  |
| LED8        | Green | System Healthy LED  0.5Hz blinking for system good  2Hz blinking if any failure events detected, etc. voltages, FAN, and temperatures failed   |
| LED 5/4/3/2 | Red   | Link matching LED for ports in SFF8674 connectors  Case 1: set in mode 1, 5 or 9  LED1 lights when port in SFF8674 not link at x16.  Case 2: set in mode 2 or 6  LED5 or/and LED3 light when ports in SFF8674 not link at x8  Case 3: set in mode 3,4,7 or 8  LED5, LED4, LED3 or/and LED2 light when ports in SFF8674 not link at x4 or 2x2 |



# **SFF8674 Pin Definition (SC mode)**



|        | DOW | Column    |           |        |        |        |        |  |
|--------|-----|-----------|-----------|--------|--------|--------|--------|--|
|        | ROW | 1         | 2         | 4      | 5      | 7      | 8      |  |
| CONN_0 | Α   | CLK_3_N   | CLK_3_P   | PERP0  | PERN0  | PERP3  | PERN3  |  |
|        | В   | CLK_7_N   | CLK_7_P   | PERP1  | PERN1  | PERP2  | PERN2  |  |
|        | С   | I2C_SCL_4 | I2C_SDA_4 | PETP0  | PETN0  | PETP3  | PETN3  |  |
|        | D   | PERST#_6  | PERST#_7  | PETP1  | PETN1  | PETP2  | PETN2  |  |
| CONN_1 | Α   | CLK_2_N   | CLK_2_P   | PERP4  | PERN4  | PERP7  | PERN7  |  |
|        | В   | CLK_6_N   | CLK_6_P   | PERP5  | PERN5  | PERP6  | PERN6  |  |
|        | С   | I2C_SCL_3 | I2C_SDA_3 | PETP4  | PETN4  | PETP7  | PETN7  |  |
|        | D   | PERST#_4  | PERST#_5  | PETP5  | PETN5  | PETP6  | PETN6  |  |
|        | Α   | CLK_1_N   | CLK_1_P   | PERP8  | PERN8  | PERP11 | PERN11 |  |
| CONN 2 | В   | CLK_5_N   | CLK_5_P   | PERP9  | PERN9  | PERP10 | PERN10 |  |
| CONN_2 | С   | I2C_SCL_2 | I2C_SDA_2 | PETP8  | PETN8  | PETP11 | PETN11 |  |
|        | D   | PERST#_2  | PERST#_3  | PETP9  | PETN9  | PETP10 | PETN10 |  |
| CONN_3 | Α   | CLK_0_N   | CLK_O_P   | PERP12 | PERN12 | PERP15 | PERN15 |  |
|        | В   | CLK_4_N   | CLK_4_P   | PERP13 | PERN13 | PERP14 | PERN14 |  |
|        | С   | I2C_SCL_1 | I2C_SDA_1 | PETP12 | PETN12 | PETP15 | PETN15 |  |
|        | D   | PERST#_0  | PERST#_1  | PETP13 | PETN13 | PETP14 | PETN14 |  |

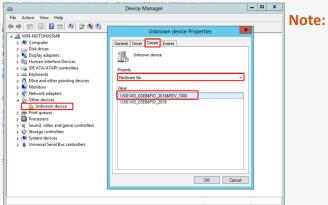


#### **USB Driver Installation**

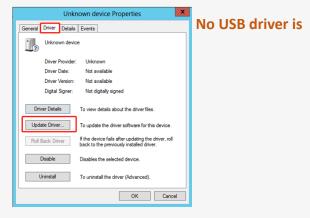
Download and install the CDC driver for unidentified device (VID 03EB&PID 2018)

Available at:

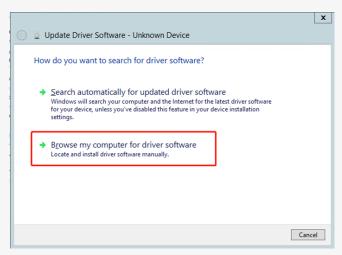
https://www.serialcables.com/wp-content/uploads/2018/11/SynergyUSBCDC 20180518.rar



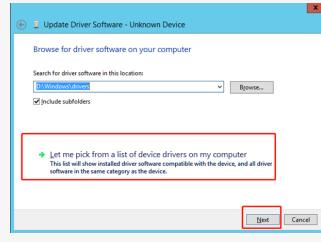
[Figure 1]



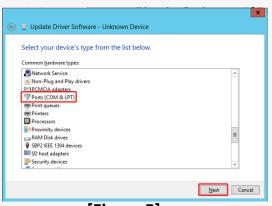
[Figure 2]



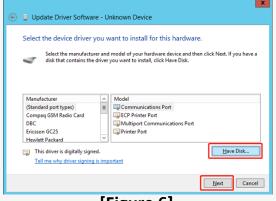
[Figure 3]



[Figure 4]



[Figure 5]



[Figure 6]





[Figure 7]



[Figure 8]



[Figure 9]



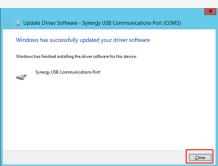
[Figure 10]



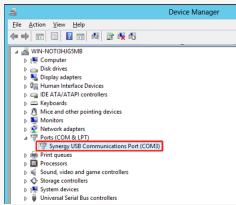
[Figure 11]



[Figure 12]



[Figure 13]



[Figure 14]

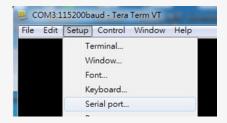


### **CLI Setup**

Step 1. Install and launch Tera Term application



**Step 2:** To ensure proper communications between host adapter card and the VT100 Terminal emulation, please configure the VT100 Terminal emulation settings to the values shown below:

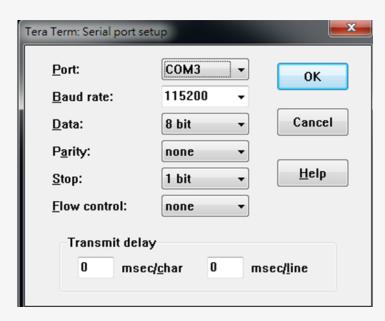


#### Step 3:

For "Port", select COM3 in this example. (Depend on which COM port used on Host) For "Baud rate", select 115200.

For "Data", select 8 bit. For "Parity", select none.

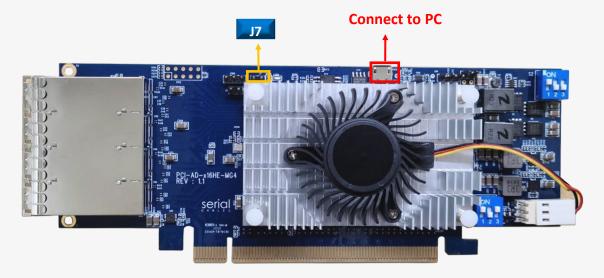
For "Stop", select 1 bit. For "Flow control", select: none.





### **FW Upgrading**

**Step 1.** Have jumper J7 ON to force uP entering FW upgrading mode.



**Step 2**: Install host adapter card into PCle slot of server, and connect Micro USB port to PC which used for FW upgrading, then power on the server.

#### Step 3.

- a.) it will show an added USB device in PC or laptop.
- b.) Put upgrading FW(i.e us\_ms\_external\_host\_card\_v001.srec) into the folder of FW.
- c.) Put update.txt in the root folder.

| 名稱              | 日期                | 類型    | 大小 | 時間 |
|-----------------|-------------------|-------|----|----|
| ■ Config        | 2017/1/1 上午 12:00 | 福案資料夾 |    |    |
| ₩ PW            | 2017/1/1 上午 12:00 | 檔案資料夾 |    |    |
| Web             | 2017/1/1 上午 12:00 | 檔案資料夾 |    |    |
| device_info.txt | 2017/1/1 上午 12:00 | 文字文件  | 1  | KB |
| update.txt      | 2018/2/9 下午 06:02 | 文字文件  | 1  | KB |

Step 4. Power cycle host card to apply the new FW.



#### **Commands List**

```
File Edit Setup Control Window KanjiCode Help
Cmd>help
   Cmd Help Menu
fdl:
               Xmodem download image.
- Usage: fdl <fw>
- fw : update fw into switch.
                Show environmental conditions information. - Usage: Isd
      ssdrst :
Reset con.
               Reset con.
- Usage: ssdrst <con(D)|a||> [channel(C)]
- con(D): con number should be 0 ~ 4
- channel(C): channel should be a or b
- Ex: ssdrst 1
- Ex: ssdrst 1 a
- Ex: ssdrst a||
- Ex: ssdrst a|| a
      showport :
Display link speed and link width information.
                - Usage: showport
               Show mode information of Switchtec port bifurcation.
                - Usage: showmode
      scan :
Scan device of i2c bus.
      clk:
Set PCIe clock output enable.
- Usage: clk [en|dis]
      iicwr:
   icwr <Addr(H)> <Con(D)> <ReadByte(D)> <WriteData(H)>
   - Addr(H): Device address
   - Con(D): Con should be 1 ~ 4
   - ReadByte(D): Max read byte is 32 byte
   - WriteData(D): Max write byte is 32 byte
   - Ex: iicwr d4 1 8 0
      iicw:
iicw <Addr(H)> <Con(D)> <WriteData(H)...>
- Addr(H): Device address
- Con(D): Con should be 1 ~ 4
- WriteData(D): Max write byte is 32 byte
- Ex: iicw d4 1 ff
      ver:
Show microcontroller firmware version.
                - Usage: ver
       toggle:
                Toggle firmware and config partitions.
                - Usage: toggle
       reset :
               .
System reset.
- Usage: reset
```



#### fdl Command

Update the configuration file or firmware for Microchip Switchtec switch.

Usage: fdl fw

```
File Edit Setup Control Window KanjiCode Help

fdl:

Xmodem download image.

- Usage: fdl <fw>
- fw : update fw into switch.
```

Note: The host card must be reset in every time FW or configuration file upgrading.

It will show error message if no reset after 1<sup>st</sup> time and continue to have 2<sup>nd</sup> upgrading.

#### **Isd Command**

Shows temperatures, FAN speed, voltages, and side-band mode support. Usage: lsd

```
File Edit Setup Control Window KanjiCode Help
Cmd>lsd

Thermal:
    Board Temperature 1: 48 degree
Switchtec Temperature 2: 48 degree
Fan Speed:
    Switch Fan: 4036 rpm

Voltage Sensor:
    12V Voltage: 12089 mV
    1.8V Voltage: 1808 mV
    0.84V Voltage 1: 838 mV
    0.84V Voltage 2: 848 mV

Side-Band Mode: SC
```



#### ssdrst Command

Issue PERST# from uP to device

-Usage: ssdrst <con(D)|all> [channel(C)]

con(D): con number should be 0 ~ 4

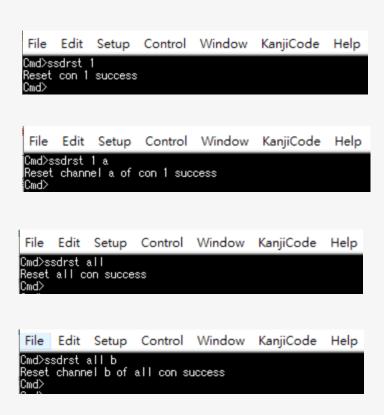
channel(C): channel number should be a or b

Channel a: The 1<sup>st</sup> PHY of dual port drive

Channel b: The 2<sup>nd</sup> PHY of dual port drive



CON<sub>0</sub>





#### showport Command

Shows ports link speed and link width information.

Usage: showport

#### Mode 1 or 5

```
File Edit Setup Control Window KanjiCode Help

Cmd>showport

Host mode

UPS: Con 0: speed = Gen3, width = 16, max_width = 16

DSP: Con 1: speed = Gen1, width = 0, max_width = 16
```

#### Mode 2 or 6

```
File Edit Setup Control Window KanjiCode Help

Cmd>showport

Host mode

UPS: Con 0: speed = Gen3, width = 16, max_width = 16

DSP: Con 1: speed = Gen1, width = 0, max_width = 8

DSP: Con 3: speed = Gen1, width = 0, max_width = 8
```

#### Mode 3 or 7

```
File Edit Setup Control Window KanjiCode Help

Cmd>showport

Host mode

UPS: Con 0: speed = Gen3, width = 16, max_width = 16

SP: Con 1: speed = Gen1, width = 0, max_width = 4

DSP: Con 2: speed = Gen1, width = 0, max_width = 4

DSP: Con 3: speed = Gen1, width = 0, max_width = 4

DSP: Con 4: speed = Gen1, width = 0, max_width = 4

DSP: Con 4: speed = Gen1, width = 0, max_width = 4
```

#### Mode 4 or 8

#### Mode 9

```
File Edit Setup Control Window KanjiCode Help
Cmd>showport
Target mode

DSP: Con 0: speed = Gen1, width = 16, max_width = 16

UPS: Con 1: speed = Gen1, width = 0, max_width = 16
```



#### **Showmode**

Shows port bifurcation mode, support up to 6 modes.

Usage: showmode



#### **Scan Command**

Scan all I2C devices in MS Slim host card

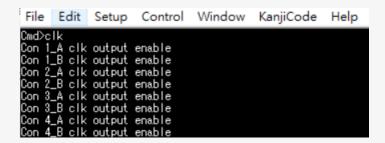
Usage: scan

```
File Edit Setup Control Window KanjiCode Help
Cmd>scan

Scan I2C channel 0 devices ....
Device address:0x42 found
Device address:0x46 found
Device address:0xa2 found
Device address:0xa2 found
Device address:0xa2 found
```

#### clk Command

Show the clock output status or disable the clock output for all downstream ports. Usage: clk



Usage: clk dis/en

Clock output disable/enable feature is dynamically changing, without card reset or power cycle.

```
File Edit Setup Control Window KanjiCode Help
Cmd>clk dis
OK, clock output disable
Cmd>
```



#### iicwr Command

Data read for drives from SMbus

Usage: iicwr <Addr(H)> <Slot(D)> <ReadByte(D)> <WriteData(H)>

- Addr(H): Device address

- con(D): con should be 1  $\sim$  4

- ReadByte(D): Max read byte is 32 byte

- WriteData(D): Max write byte is 32 byte

- Ex: iicwr d4 180

```
File Edit Setup Control Window KanjiCode Help

Cmd>iicwr d4 1 8 0

Data [0] = 6

Data [1] = 7b

Data [2] = 1f

Data [3] = 1a

Data [4] = 0

Data [5] = 0

Sata [6] = 0

Sata [7] = 26
```

#### iicw Command

Byte or page write data to drives from SMbus

Usage: iicw <Addr(H)> <Slot(D)> <WriteData(H)>

- Addr(H): Device address

- con(D) : con should be  $1 \sim 4$ 

- WriteData(D): Max write byte is 32 byte

- Ex: iicw d4 1 ff

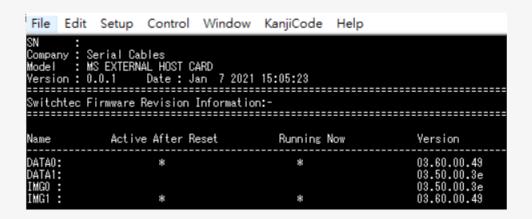
```
<u>F</u>ile <u>E</u>dit <u>S</u>etup C<u>o</u>ntrol <u>W</u>indow <u>K</u>anjiCode <u>H</u>elp
Cmd>i i cw d4 1 ff
Write Data [0] = ff
```



#### ver Command

Shows card information, S/N, uP FW and PCIe switch Switchtec FW version.

Usage: ver



#### toggle Command

Toggle firmware and config partitions

Usage: reset



#### reset Command

Reset uP FW Usage: reset

